

# O-DU Low

## General Information

| O-RAN Distributed Unit Low Layers (O-DU LOW) |   |
|--|---|
| PTL  | @Farias Luis  |
| Summary                                      | Focus on L1 functional development according to O-RAN WGs and 3GPP specification. |
| Project                                      | <a href="#">o-du</a> , <a href="#">o-du/l2</a> , <a href="#">o-du/phy</a>         |

## Quick Explore

### General Introduction

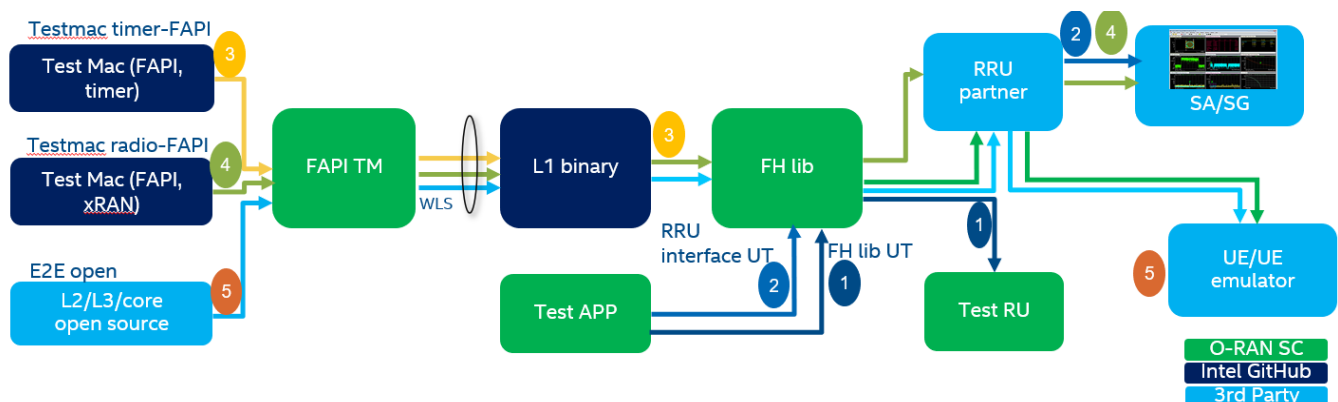
#### The project include two parts contributions

part 1: O-RAN FH interface lib, FAPI TM lib and WLS lib. O-RAN FH interface lib which follow O-RAN WG4 interface to implement O-RAN radio interface from DU end, WLS and FAPI TM is the interface implementation between O-DU low and O-DU High.

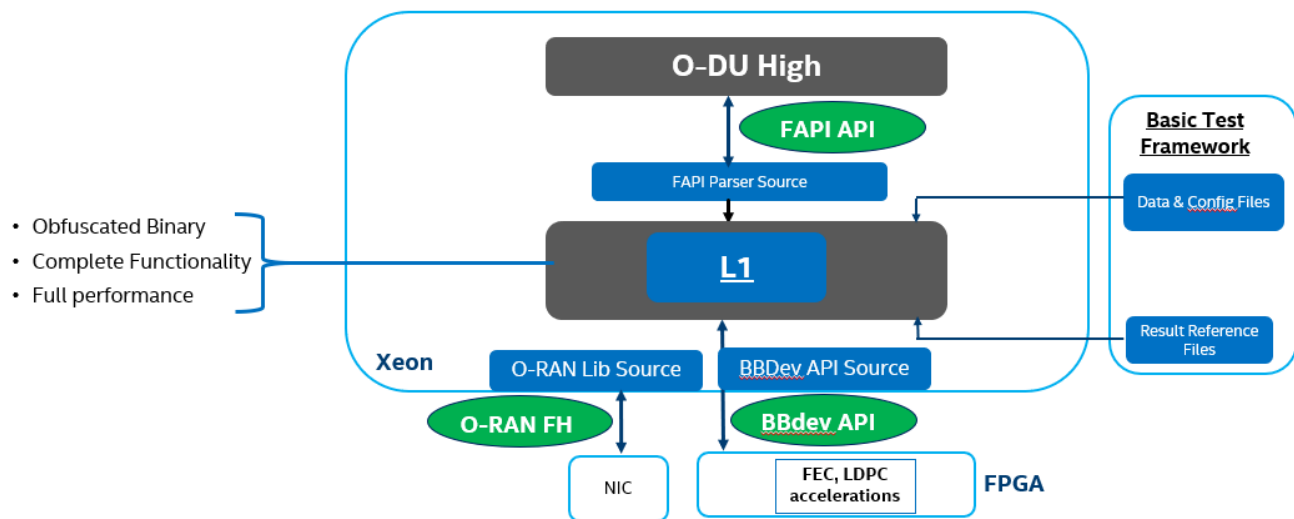
part 2: L1 binary along with one set of uses cases which were performed with test MAC application.

With above parts, below O-RAN network functions can be verified with different data flow to meet your different purpose:

- data flow 1: FH lib UT, FH lib had it's own test framework, which includes one test app to simulator the L1 and test RU to simulator the RU, between test app and test RU, data was exchanged follow O-RAN WG4 C/U/S plane spec. there are multiple use cases that are defined with variant numerology and bandwidth.
- data flow 2: RRU interface UT, the test app and FH lib can also be used to test real RRU with simulated L1 data connecting to SA/SG
- data flow 3: testmac timer: L1 binary and test mac timer mode was provided to verify the FAPI interface with full L1 pipeline/functionalities , it also can be used to verify the HW platform supportive of the L1 SW with proper capabilities.
- data flow 4: testmac radio: With full L1 pipeline integrated FH lib, this data flow can be used to verify the RRU with full L1 pipeline.
- data flow 5: E2E open source: together with other open source project in OSC, E2E data flow can be verified, please go to [Integration and Testing and Requirements and Software Architecture Committee Home](#) for detail E2E integration current status and plan.



below picture shows the architecture and the interface of O-DU low. this project mainly focus on the open interface development so far, three interfaces will be involved to interact with L1.



### Interfaces: